

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions and listing of claims in the above-referenced application.

Listing of Claims:

1. (Currently Amended) A method of managing data in a cache, comprising:

providing a first cache memory containing data;

providing a second cache memory containing data, wherein at least some of the data contained in the first cache memory is the same as at least some of the data contained in the second cache memory, wherein data contained in said first and said second cache memories includes control data and corresponding disk data, ~~wherein, when said corresponding disk data has not been modified,~~ said control data [[is]] being replicated in both said first and said second cache memories regardless of a modification status of said corresponding disk data; and

in response to a request for data that is stored in both the first cache memory and the second cache memory, selecting which one of the first and second cache memories to use to obtain the requested data in accordance with an access balancing technique.

2. (Previously presented) A method, according to claim 1, wherein the access balancing technique includes at least one of: selection using round robin and selection based on statistical analysis.

3. (Previously presented) A method, according to claim 2, wherein the selection based on statistical analysis includes:

monitoring a number of accesses of the first cache memory over a predetermined amount of time;

monitoring a number of accesses of the second cache memory over the predetermined amount of time; and

selecting one of the cache memories having a lowest number of accesses over the predetermined amount of time.

4. (Cancelled)

5. (Previously Presented) A method, according to claim 1, wherein a first balancing technique is used to balance access to the control data and a second balancing technique is used to balance access to the disk data.

6. (Original) A method, according to claim 5, wherein the first and second balancing techniques are the same.

7. (Previously presented) A method, according to claim 6, wherein the first and second access balancing techniques include at least one of: selection using round robin and selection based on statistical analysis.

8. (Previously Presented) A method of managing data in a cache, comprising:

providing a first cache memory containing data;

providing a second cache memory containing data, wherein at least some of the data contained in the first cache memory is the same as at least some of the data contained in the second cache memory;

in response to a request for data that is stored in both the first cache memory and the second cache memory, selecting which one of the first and second cache memories to use to obtain the requested data in accordance with an access balancing technique; and

subdividing the data contained in said first and said second cache memories into control data and corresponding disk data, wherein a first balancing technique is used to balance access to the control data and a second balancing technique is used to balance access to the disk data, and wherein the first and second balancing techniques are different.

9. (Previously presented) A method, according to claim 8, wherein the first access balancing technique includes at least one of: selection using round robin and selection based on statistical analysis.

10. (Previously presented) A method, according to claim 9, further comprising:

in response to selecting the first cache memory for accessing the control data, accessing the second cache memory for the corresponding disk data; and

in response to selecting the second cache memory for accessing the control data, accessing the first cache memory for the corresponding disk data.

11. (Previously presented) A method, according to claim 9, wherein the second access balancing technique includes at least one of: selection using round robin and selection based on statistical analysis.

12. (Previously Presented) A method, according to claim 1, wherein selecting which one of the first and second cache memories includes using specialized hardware.

13. (Original) The method of Claim 12, wherein the specialized hardware includes at least one chip.

14. (Previously Presented) The method of Claim 12, further comprising:

receiving, by the specialized hardware, requests for data from at least one of the first and second cache memories.

15. (Original) The method of Claim 14, further comprising:

processing, by the specialized hardware, the requests in accordance with the access balancing technique.

16. (Previously Presented) The method of Claim 15, further comprising:

subdividing the data contained in said first and second cache memories into control data and corresponding disk data.

17. (Original) The method of Claim 16, further comprising:

using the specialized hardware to access at least one of said control data and said corresponding disk data.

18. (Currently Amended) A system for managing data in a cache comprising:

a first cache memory containing data;

a second cache memory containing data wherein at least some of the data contained in the first cache memory is the same as at least some of the data contained in the second cache memory, wherein data contained in said first and said second cache memories includes control data and corresponding disk data, ~~wherein, when said corresponding disk data has not been modified, said control data~~ being replicated in both said first and said second cache memories regardless of a modification status of said corresponding disk data independent of whether said corresponding disk data is included in both said first and said second cache memories; and

cache selection hardware for selecting, in response to a request for requested data that is stored in both the first cache memory and the second cache memory, which one of the first and second cache memories to use to obtain the requested data in accordance with an access balancing technique.

19. (Previously Presented) The system of Claim 18, wherein said access balancing technique is a first access balancing technique and the system further comprises:

machine executable code for selecting which one of the access balancing technique and a second access balancing technique to use to obtain data stored in both the first and second cache memories;

machine executable code for selecting, in response to a request for requested data that is stored in both the first cache memory and the second cache memory and selecting said second

access balancing technique, which one of the first and second cache memories to use to obtain the requested data in accordance with said second access balancing technique.

20. (Cancelled)

21. (Previously Presented) The system of Claim 18, wherein said cache selection hardware is used in implementing one of the first and second access balancing techniques, and the machine executable code is used in implementing a different one of the first and second balancing techniques than the cache selection hardware.

22. (Previously Presented) The system of Claim 18, wherein said cache selection hardware is used in implementing at least one of said first and second balancing techniques, and the machine executable code is used in implementing at least one of said first and second balancing techniques.

23. (Previously presented) The system of Claim 21, wherein said first and second access balancing techniques include at least one: selection using round robin and selection based on statistical analysis.

24. (Previously presented) The system of Claim 22, wherein said first and second access balancing techniques include at least one: selection using round robin and selection based on statistical analysis.

25. (Previously Presented) The system of Claim 24, wherein the cache selection hardware selects the first cache memory for accessing the control data; and

selects the second cache memory for the disk data corresponding to the control data when said first cache memory is used for accessing the control data.

26. (Previously Presented) The system of Claim 25, wherein the cache selection hardware:

selects the second cache memory for accessing the control data; and

accesses the first cache memory for the disk data corresponding to the control data when said second cache memory is used for accessing the control data.

27. (Previously presented) The system of Claim 24, wherein said cache selection hardware selects the first cache memory for accessing the control data; and

accesses the second cache memory for the disk data corresponding to the control data when said first cache memory is used for accessing the control data.

28. (Presently presented) The system of Claim 27, wherein said cache selection hardware selects the second cache memory for accessing the control data; and

accesses the first cache memory for the disk data corresponding to the control data when said second cache memory is used for accessing the control data.

29. (Previously presented) The system of Claim 18, further comprising:

a first bus used for sending communications between at least one of the first cache memory and the second cache memory, and the cache selection hardware; and

a second bus used for sending communications between at least one of the first cache memory and the second cache memory, and the cache selection hardware.

30. (Previously presented) The system of Claim 29, wherein said first bus is used for facilitating communications between said cache selection hardware and said first cache memory, and said second bus is used for facilitating communications between said cache selection hardware and said second cache memory.

31. (Currently Amended) A computer program product ~~[[stored on]]~~ including a computer readable medium for managing data in a cache, the computer readable medium comprising:

processor executable code for providing a first cache memory containing data;

processor executable code for providing a second cache memory containing data, wherein at least some of the data contained in the first cache memory is the same as at least some of the data contained in the second cache memory, wherein data contained in said first and said second cache memories includes control data and corresponding disk data, ~~wherein, when said corresponding disk data has not been modified,~~ said control data ~~[[is]]~~ being replicated in said first and said second cache memories regardless of a modification status of said corresponding disk data ~~independent of whether said corresponding disk data is included in both said first and said second cache memories;~~ and

processor executable code for, in response to a request for data that is stored in both the first cache memory and the second cache memory, selecting which one of the first and second cache memories to use to obtain the requested data in accordance with an access balancing technique.

32. (Previously presented) The computer program product, according to Claim 31, wherein the processor executable code for selecting which one of the first and second cache memories to use to obtain the data in accordance with an access balancing technique, includes processor executable code for at least one of: selection using round robin and selection based on statistical analysis.

33. (Previously presented) The computer program product, according to claim 32, wherein the processor executable code for selection based on statistical analysis, includes processor executable code for:

monitoring a number of accesses of the first cache memory over a predetermined amount of time;

monitoring a number of accesses of the second cache memory over the predetermined amount of time; and

selecting one of the cache memories having a lowest number of accesses over the predetermined amount of time.

34. (Cancelled).

35. (Currently Amended) The computer program product, according to claim 31, wherein the computer readable medium further [[comprising]] comprises processor executable code for a first balancing technique used to balance access to the control data and a second balancing technique used to balance access to the disk data.

36. (Original) The computer program product, according to claim 35, wherein the first and second balancing techniques are the same.

37. (Previously presented) The computer program product, according to claim 36, wherein the processor executable code for the first and second access balancing techniques includes processor executable code for at least one of: selection using round robin and selection based on statistical analysis.

38. (Original) The computer program product, according to claim 35, wherein the first and second balancing techniques are different.

39. (Previously presented) The computer program product, according to claim 38, wherein the processor executable code for the first access balancing technique includes processor executable code for at least one of: selection using round robin and selection based on statistical analysis.

40. (Currently Amended) The computer program product, according to claim 39, wherein the computer readable medium further [[comprising]] comprises processor executable code for:

in response to selecting the first cache memory for accessing the control data, accessing the second cache memory for the corresponding disk data; and

in response to selecting the second cache memory for accessing the control data, accessing the first cache memory for the corresponding disk data.

41. (Previously presented) The computer program product, according to claim 39, wherein the processor executable code for the second access balancing technique includes processor executable code for at least one of: selection using round robin and selection based on statistical analysis.

42. (Previously presented) The method of Claim 1, wherein, when said corresponding disk data has not been modified and said control data is replicated in said first and said second cache, said corresponding disk data is only in one of said first and second cache memories.

43. (Previously presented) The system of Claim 18, wherein, when said corresponding disk data has not been modified and said control data is replicated in said first and said second cache, said corresponding disk data is only in one of said first and second cache memories.

44. (Previously presented) The computer program product of Claim 31, wherein, when said corresponding disk data has not been modified and said control data is replicated in said first and said second cache, said corresponding disk data is only in one of said first and second cache memories.